

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hongyong Zhang  
Serial No. : 09/362,808  
Filed : July 28, 1999  
Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Art Unit : 2814  
Examiner : Shrinivas H. Rao  
Conf. No. : 7320

**MAIL STOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Applicants request consideration of the reference listed on the attached PTO-1449 form.

This statement is being filed after a final Office action or a Notice of Allowance, but before payment of the issue fee. The information in this statement (i) was cited in a communication from a foreign patent office in a counterpart foreign application, the communication being dated June 12, 2007, which is not more than three months prior to the filing of this statement, and (ii) was not first cited in any communication from a foreign patent office in a counterpart application which was more than three months prior to the filing date of this statement. The \$180 late submission fee under §1.17(p) is being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,


Date: \_\_\_\_\_

9/12/07

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